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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Keiji Negi  
 Serial No. : Not yet assigned  
 Filed : November 27, 2000  
 Title : PATTERN SYNCHRONOUS CIRCUIT

Art Unit : Unknown  
 Examiner : Unknown

Commissioner for Patents  
 Washington, D.C. 20231

Box Patent Application

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

In the Claims: A marked-up version of the amended claims is provided on separate attached sheets.

Please amend the claims as follows:

4. (Amended) The pattern synchronous circuit as defined in claim 2 wherein said shift means shifts bits without sorting a list of the parallel signals according to the frame position information.
5. (Amended) The pattern synchronous circuit as defined in claim 2 wherein said sort means sorts a list of bits in the same clock of the parallel signals according to the frame position information.

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